

O.I.P. = J.C.R.
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PATENT & TRADEMARK OFFICE

Fee Only

First Named Inventor	Leonard Forbes
Serial No.	10/682,590
Filing Date	October 9, 2003
Group Art Unit	2811
Examiner Name	Unknown
Confirmation No.	1986
Attorney Docket No.	400.257US01

INFORMATION
DISCLOSURE
STATEMENT

Title: FULLY DEPLETED SILICON-ON-INSULATOR CMOS LOGIC

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Pursuant to 37 C.F.R. 1.98 (a)(2)(i), as this application was filed after June 30, 2003, Applicant has not included copies of U.S. Patents or U.S. Patent Applications. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

As an Office Action has not yet issued in this application, Applicant believes that no fees are due. However, the Commissioner for Patents is hereby authorized to charge any additional fees to Deposit Account No. 501373. If the Examiner has any questions or concerns regarding this application, please contact the undersigned at (612) 312-2211.

Respectfully submitted,

Date: 5/24/04

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